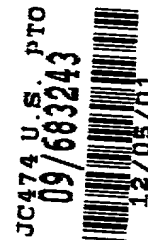


Electronic Filing System (EFS) Data
Electronic Patent Application Submission
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EFS ID: 13172
Application ID: 09683243
Title of Invention: Integrated level shift and override
circuit for a processor system
First Named Inventor: Chia-Hsing Yu
Domestic/Foreign Application: Domestic Application
Filing Date: null
Effective Receipt Date: 2001-12-05
Submission Type: Utility Patent Filing
Filing Type: new-utility
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Digital Certificate Holder: cn=Winston Hsu, ou=Registered Attorneys, ou=Patent and
Trademark Office, ou=Department of Commerce, o=U.S.
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Certificate Message Digest: 5DBHyQJu1sJVQIL5+D0OMQ==
Total Fees Authorized: \$780.0
Payment Category: DA - Deposit Account
Deposit Account Number: 500801
Deposit Account Name: WINSTON HSU



TRANSMITTAL FORM



Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number: VIAP0010USA

Integrated level shift and override circuit for a processor system

First Named Inventor: Chia-Hsing Yu

SUBMITTED BY

Name:	Mr. WINSTON HSU
Registration Number:	41,526
Electronic Signature Mark: Winston Hsu	Date Signed: 20011205

I certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.

I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

Attached Files:

bibd-transmittal	VIAP0010apds.xml
fee-transmittal	VIAP0010fee.xml
patent-assignment	VIAP0010asgn.xml
specification	VIAP0010.xml
declaration	VIAP0010DEC1.tif
declaration	VIAP0010DEC1.tif

Attached Image File(s):

VIAP0010DEC1.tif

VIAP0010DEC2.tif

09683243 APP ID

Comments:

09683243

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor , I hereby declare that :

I believe I am the sole (if only one name appears below) , or joint (if more than one name appears) , original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled :

"Integrated level shift and override circuit for a processor system"

 + The specification for the above entitled invention is filed herewith.

 The specification for the above entitled invention was filed previously

with application serial number: Filing Date:

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of the invention disclosed in this application in accordance with Title 37, Code of Federal Regulations, Section 1.56 (a). I further acknowledge the duty in any continuation-in-part application to disclose to the Patent and Trademark Office all information known to be material to the patentability of the invention disclosed in this application, as defined in 1.56, which became available to me between the filing date of the prior application and the filing date of this application.

PRIORITY CLAIM

 There is no claim of priority

 + Claim of priority is based on the following:
Provisional Application No. = 60/255,618
Filing Date = 2000/12/13

POWER OF ATTORNEY

As a named inventor, I hereby appoint the following attorney to prosecute this application and to transact all related business in the Patent and Trademark Office:

Winston Hsu, Registration Number 41,526
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Taipei Hsien, Taiwan, R.O.C.
TEL: +886-2-8923-7350

DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued hereon.

Date: 11.29.01 Chia-Hsing Yu
Printed Name: Chia-Hsing Yu
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and Residence: City, Taipei Hsien, Taiwan, R.O.C.
Citizen of: R.O.C.
Date: 11.29.01 Nai-Shung Chang
Printed Name: Nai-Shung Chang
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and Residence: Chung-Ho City, Taipei Hsien, Taiwan, R.O.C.
Citizen of: R.O.C.

FEE TRANSMITTAL

Electronic Version 1.1.0

Stylesheet Version: 1.0

Patent fees are subject to annual revisions on or about October 1st of each year.

Large Entity

TOTAL FEES AUTHORIZED: \$ 780

The commissioner is hereby authorized to charge indicated processing and/or publication fees and credit any overpayments to:

Deposit Account Number: 500801

Deposit Account Name: NORTH AMERICA INTERNATIONAL PATENT OFFICE

Charge Any Additional Fee Required Under 37 C.F.R. Sections 1.16 and 1.17.

SUBMITTED BY

Authorized Name: WINSTON HSU
Electronic Signature Mark: VAEB-JMXX-8IIL
Date Signed: 20011205

BASIC FILING FEE

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	101	\$ 740

Subtotal For Basic Filing Fee: \$ 740

EXTRA CLAIM FEES

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 15	103	\$ 18	0	\$ 0
Independent Claims: 1	102	\$ 84	0	\$ 0

Subtotal For Extra Claims Fees: \$ 0

ADDITIONAL FEES

Fee Description	Fee Code	Fee Paid
Recording Each Patent Assignment Per Property Fee	581	\$ 40

Subtotal For Additional Fees: \$ 40

09683243